

Notice of References Cited	Application/Control No. 10/750,150	Applicant(s)/Patent Under Reexamination HAMMOND ET AL.	
	Examiner Benjamin P. Geib	Art Unit 2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,658,559	12-2003	Arora et al.	712/245
*	B	US-2004/0168045	08-2004	Morris et al.	712/225
*	C	US-6,598,156	07-2003	Arora, Judge K.	712/244
*	D	US-6,889,315	05-2005	Miyake et al.	712/217
	E	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"Itanium 2 Processor Microarchitecture"; Cameron McNairy and Don Soltis; IEEE; March 2003
	V	"ARB: A Hardware Mechanism for Dynamic Reordering of Memory References"; Manoj Franklin and Gurindar Sohi; IEEE; 1996
	W	"Computer Architecture: A Quantitative Approach" John Hennessy and David Patterson; May 2002
	X	"Superscalar Microprocessor Design"; Mike Johnson; 1991

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.